



Image

2316

JP

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Hiroyoshi TOMITA

Serial No.: 10/078,340

Filed: February 21, 2003

Confirmation No.: 6076

Group Art Unit: 2816

Examiner: Minh Nguyen

Atty. Docket No.: 108397-00067

For: SEMICONDUCTOR INTEGRATED CIRCUIT, METHOD OF CONTROLLING
THE SAME, AND VARIABLE DELAY CIRCUIT

SUPPLEMENTAL AMENDMENT UNDER 37 CFR § 1.111

Director for the U.S. PTO
P.O. Box 1450
Alexandria, VA 22313-1450

January 12, 2004

Sir:

In reply to a telephone interview with the Examiner on December 29, 2003,
please consider the following: